

IN THE SPECIFICATION

Please replace the paragraph at page 1, line 4, to line 14, with the following.
Added text is indicated by underlining, and deleted text is indicated by ~~strikethrough~~.
Changes are identified by vertical change bars in the left margin.

Cross Reference to Related Applications

~~This Application claims priority to pending~~ The present application is a divisional
application of U.S. Application No. 10/020,569 filed December 7, 2001, entitled
"Reference Wafer and Process for Manufacturing Same", by Adlai Smith, Bruce
McArthur, and Robert Hunter, which claims priority to U.S. provisional application serial
No. 60/254,271 filed December 8, 2000, and U.S. patent application serial No.
09/835,201 filed April 13, 2001, both applications entitled "Method And Apparatus For
Self-Referenced Projection Lens Distortion Mapping", by Adlai Smith, Bruce McArthur,
and Robert Hunter; U.S. provisional application serial No. 60/254,413 filed December 8,
2000 and U.S. patent application serial No. 09/891,699 filed June 26, 2001, both
applications entitled "Method And Apparatus For Self-Referenced Wafer Stage
Positional Error", to Adlai Smith, Bruce McArthur, and Robert Hunter; and U.S.
provisional application serial No. 60/254,315 filed December 8, 2000 entitled "Reference
Wafer and Process for Manufacturing Same", to Adlai Smith, Bruce McArthur, and
Robert Hunter, all of which are incorporated by reference in their entirety herein.

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